

AMENDMENTS TO THE DRAWINGS

Please replace Fig. 7 of the present application in accordance with the attached drawing.

Attachment: Replacement sheet
 Annotated sheet showing changes

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated June 28, 2007 has been received and its contents carefully reviewed.

In accordance with the foregoing, the specification, the drawings, and claims 10, 12-15 have been amended and claims 1-9 and 16-33 have been canceled. No new matter is being presented. Therefore, claims 10, 12-15 are pending and under consideration. Reconsideration is respectfully requested.

In the outstanding Office Action, the Examiner objected to the drawings under 37 CFR 1.84(p)(5) as including the following reference character not mentioned in the description: fig. 7 (34) and as not including the following reference character mentioned in the description "126" [0099].

The reference character (34) in Fig. 7 has been respectfully deleted and a replacement sheet for Fig. 7 has been attached herein. Further, the reference character "126" [0099] has been respectfully deleted from the description to comply with 37 CFR 1.84(p)(5).

In the outstanding Office Action, the Examiner rejected claims 1-15 under 35 U.S.C. 112, second paragraph. The claims have been amended making this rejection moot.

In the outstanding Office Action, the Examiner rejected claims 1-9 and 16-31, 33 under 35 U.S.C. 102(e) as being unpatentable over Lee (U.S. Patent Publication No. 2002/0005825).

The corresponding claims have been respectfully cancelled.

In the outstanding Office Action, the Examiner rejected claims 10-15 under 35 U.S.C. 103(a) as being unpatentable over Lee in view of Koyama et al (U.S. Patent No. 7,151,511). This rejection is respectfully traversed.

Claim 10 is allowable over the cited references in that claim 1 recites a combination of elements including, for example: a data signal controller circuit which includes “a first data signal controller circuit for storing a voltage corresponding to the data signals outputted by the data driver during application of a first scan signal” and “a second data signal controller circuit for storing a voltage corresponding to a data signal outputted from the first data signal controller circuit and for applying the stored voltage to the data lines between application of the first scan signal and a subsequent application of a second scan signal”; and the data signal controller circuit includes a signal supplier which includes “a shift register for driving the first data signal controller circuit and the first switch during application of the first scan signal, and a line pass controller for driving the second data signal controller circuit and the second switch between application of the first scan signal and application of the second scan signal for driving the first and second data signal controller circuit”. Lee and Koyama do not teach or suggest at least these features of the claimed invention. Accordingly, Applicants respectfully request withdrawal of the rejection of claim 10.

The Examiner pointed out that Lee teaches a shift register (fig. 12 (26)) for driving the first data signal controller circuit (28) and the first switching during (inherent) application of the first scan signal; and a line pass controller ([0101] data supplier) for driving the second data signal controller circuit (30) and the second switch (inherent) between application of the first scan signal and application of the second scan signal [0101].

However, paragraph [0101] of Lee reads “the first latch 28 responds to a shift clock from the shift register 26 to sequentially store a data supplied from a data supplier (not shown).” That is, the data supplier only supplies the data to the first latch 28, and there is no teaching or suggestion for the data supplier to drive the second data signal controller circuit.

Thus, Lee does not teach or suggest the line pass controller for driving the second data signal controller circuit and the second switch between application of the first scan signal and application of the second scan signal.

According to the claimed invention, the line pass controller drives the second data signal controller circuit by applying a turn-on signal to rendering the second and fourth switches electrically conductive. That is, the line pass controller provides the turn-on signal between the

application of the first gate-on signal and the application of a succeeding scan signal for driving a gate line, succeeding the pre-stage gate line.

However, Lee does not disclose, teach or suggest the line pass controller as described above.

Claims 11-15 depend from independent claim 10, respectively, and should be considered allowable for at least the same reasons as for claim 10. Accordingly, reconsideration and withdrawal of the objections to claim 10 and 11-15 are respectfully requested.

Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Dated: **2 October 2007**

Respectfully submitted,

By 

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Attachments